N2S065010PE2

Silicon Carbide Schottky Diode

V_{RRM}	= 650V
$I_F(T_C \leq 135℃)$	= 19A
Q_c	= 32nC

Features

- New Thin Wafer Technology
- Low Forward Voltage Drop (V_F)
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- Positive Temperature Coefficient on V_F
- Temperature-independent Switching

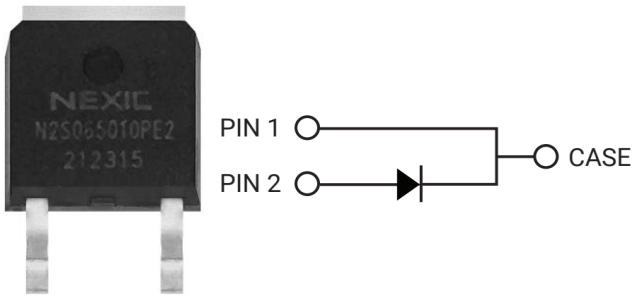
Benefits

- Replace Bipolar with Unipolar Device
- Reduction of Heat Sink Size
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses

Applications

- Switch Mode Power Supplies
- Uninterruptible Power Supplies
- Motor drive, PV Inverter, Wind Power Station

Package



TO-252

Part Number	Package	Marking
N2S065010PE2	TO-252	N2S065010PE2

Maximum Ratings

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{RRM}	Repetitive Peak Reverse Voltage	650	V	T_C = 25℃	
V_{RSM}	Surge Peak Reverse Voltage	650	V	T_C = 25℃	
V_R	DC Blocking Voltage	650	V	T_C = 25℃	
I_F	Forward Current	38 19 10	A	$T_C \leq$ 25℃ $T_C \leq$ 135℃ $T_C \leq$ 150℃	
I_{FSM}	Non-Repetitive Forward Surge Current	100	A	T_C = 25℃, t_p = 8.3ms, Half Sine Wave	
P_{tot}	Power Dissipation	125	W	T_C = 25℃	Fig.3
T_J, T_{STG}	Operating Junction and Storage Temperature	-55 to 150	℃		

Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
V_F	Forward Voltage	1.30 1.46	1.5 1.6	V	I_F = 10A, T_J = 25℃ I_F = 10A, T_J = 150℃	Fig.1
I_R	Reverse Current	1.5 5.4	50 200	μA	V_R = 650V, T_J = 25℃ V_R = 650V, T_J = 150℃	Fig.2
C	Total Capacitance	606 61 47	/	pF	V_R = 0V, T_J = 25℃, f = 1MHz V_R = 200V, T_J = 25℃, f = 1MHz V_R = 400V, T_J = 25℃, f = 1MHz	Fig.5
Q_C	Total Capacitive Charge	32	/	nC	V_R = 400V, I_F = 10A di/dt = 200A/μs, T_J = 25℃	Fig.4

Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.0	℃/W	Fig.6
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	114	℃/W	
T_{sold}	Soldering Temperature	260	℃	

Typical Performance

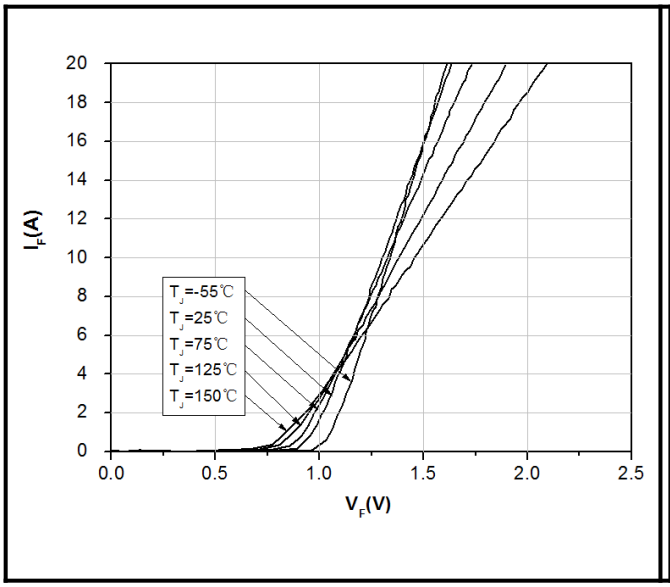


Figure 1. Forward Characteristics

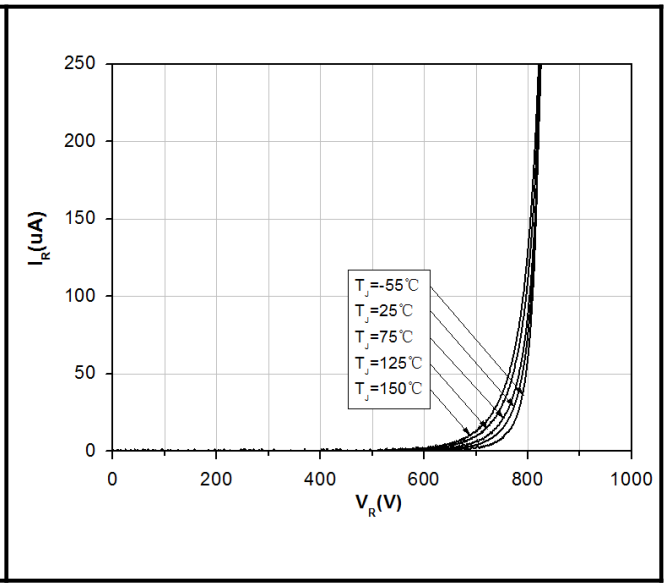


Figure 2. Reverse Characteristics

Typical Performance

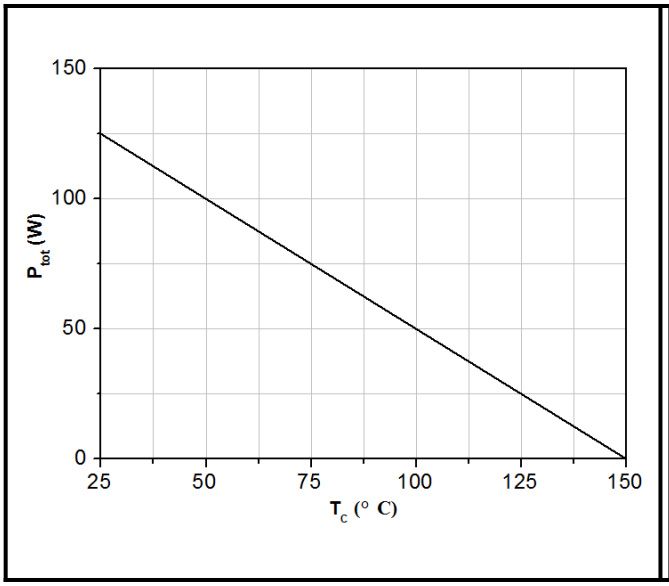


Figure 3. Power Derating

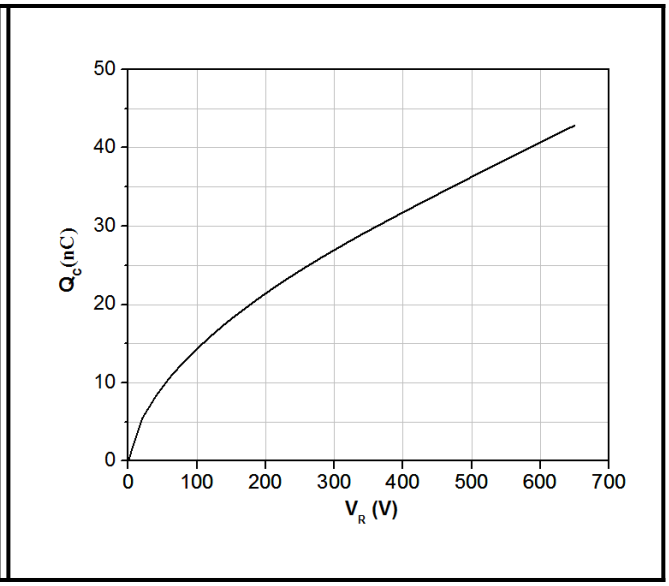


Figure 4. Total Capacitive Charge vs. Reverse Voltage

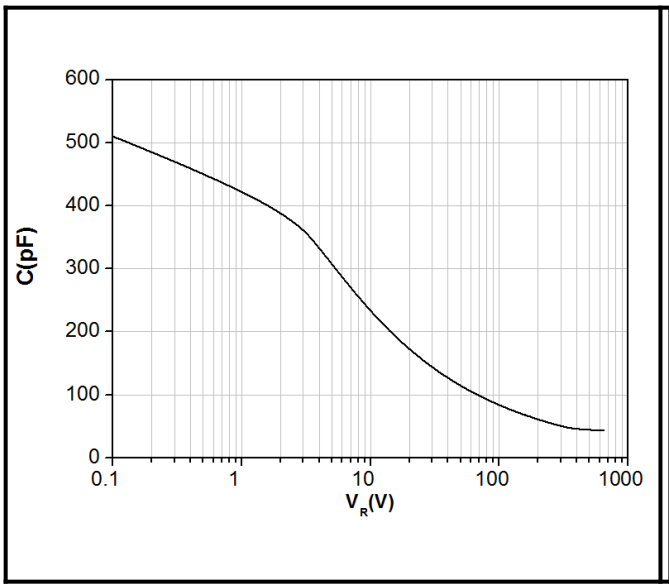


Figure 5. Total Capacitance vs. Reverse Voltage

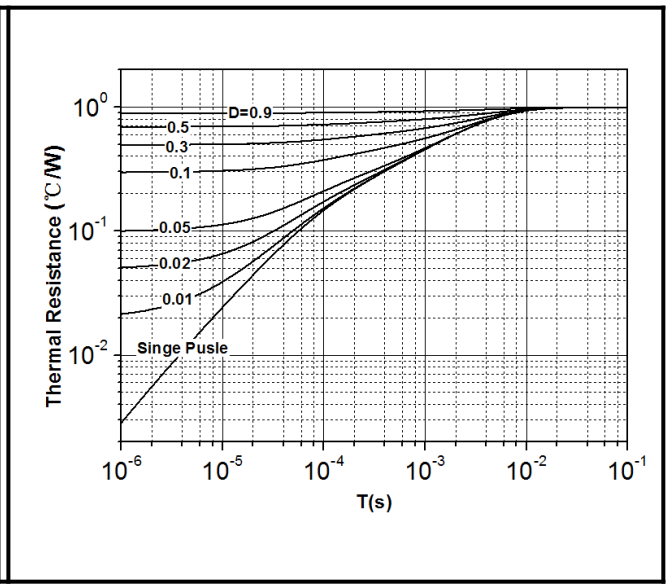
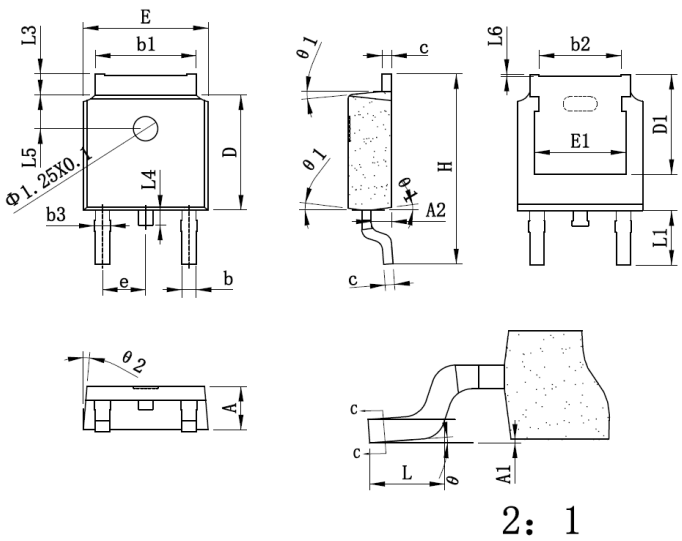


Figure 6. Transient Thermal Impedance

Package Dimensions

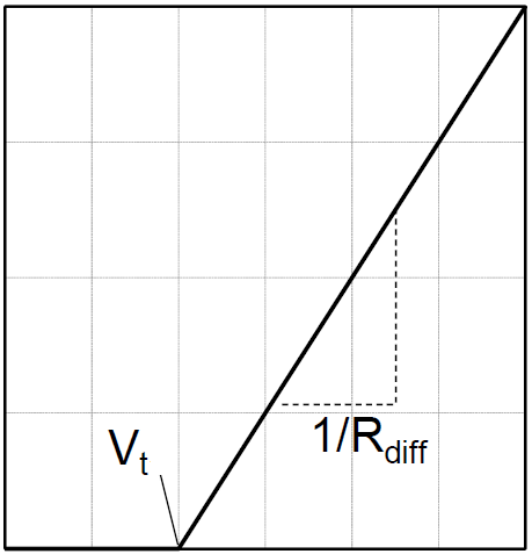
Package TO-252



SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	—	0.15
A2	0.90	1.00	1.10
b	0.72	0.78	0.85
b1	5.23	5.33	5.46
b2	4.27	4.32	4.37
b3	0.78	0.85	0.90
c	0.47	0.52	0.55
D	6.00	6.10	6.20
D1		5.40REF	
E	6.50	6.60	6.70
E1	4.70	4.83	4.92
e		2.286BSC	
H	9.90	10.10	10.20
L	1.40	1.55	1.70
L1		2.90REF	
L3	0.90	—	1.20
L4	0.75	0.85	0.95
L5	1.70	1.80	1.90
L6	0.00	0.04	0.12
θ	0°	—	5°
θ1	5°	7°	9°
θ2	5°	7°	9°

Simplified Diode Model

Equivalent IV Curve for Model



Mathematical Equation

$$V_F = V_t + I_F \times R_{diff}$$

$$V_t = -0.001 \times T_j + 0.99 \text{ [V]}$$
$$R_{diff} = 6.9 \times 10^{-7} \times T_j^2 + 4.3 \times 10^{-5} \times T_j + 0.28 \text{ [Ω]}$$

Note:

T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to150°C
I_F= Forward Current Less than 20A